Yucong HUANG (Prof. Chi-Ying TSUI) PhD, Neural Network Acceleration

Education:

- **2015 – 2019, BEng, Southern University of Science and Technology (SUSTech).**
- **2019 – Present, PhD, HKUST.**

Research Interests:

- **Neural Network Acceleration**
- **In-memory Computing**
- **Non-volatile Memory**
- **Circuit & Architecture Design**

Publications:

- Yucong Huang, Zhitao Yang, Jianghan Zhu, Terry Tao Ye, "Analog Circuit Implementation of Neurons with Multiply-Accumulate and ReLU Functions," *The 30th edition of the ACM Great Lakes Symposium on VLSI (GLSVLSI) 2020*;
- Yucong Huang, Dingxin Xu, Shuming Chen, *"Investigation of Electroluminescent Response of Quantum Dot Light Emitting Diodes," International Conference on Display Technology (ICDT) 2018.*

Core Courses Taken:

- **ELEC6910T Deep Learning**
- **ELEC5040 Adv. Analog IC Design**
- **EESM5020 Dig. VLSI System Design and Design Automation**
- **COMP5421 Computer Vision**