# Huimin ZHENG (Prof. Mansun CHAN) Computing-in-Memory Circuit Design

# Education History

### - MPhil in Electronic & Computer Engineering

Hong Kong University of Science and Technology (2022 - )

#### - BSc in Applied Physics

University of Science and Technology of China (2018 - 2022)

## Research Topics

Computing-in-Memory Circuit Design

Hobbies Trail run, Cycling